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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mark Beaumont

Examiner: Abdullah Al Kawsar

Patent No.: 7,430,742

Group Art Unit: 2195

Issue Date: September 30, 2008

Docket No: 2002-1267.00/US

Title: METHOD FOR LOAD BALANCING A LINE OF PARALLEL PROCESSING ELEMENTS

Commissioner for Patents

Office of Patent Publication

ATTN: Certificate of Correction Branch

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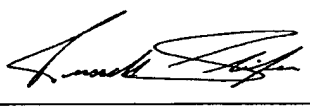
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- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
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Respectfully submitted,

Date: October 21, 2008

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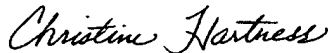
Russ Slifer

Reg. No. 39,838

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Christine Hartness

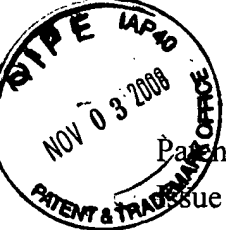
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ELEMENTS

REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 26809

Date: October 21, 2008

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Christine Hartness



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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,430,742

Page (1) of 1

DATED : September 30, 2008

INVENTOR(S) : Beaumont

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 12, line 52, in Claim 11, delete "PE_{r+1}" and insert - - PE_{r+1} - -, therefor.

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